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Virtuoso Spectre Circuit Simulator User

Virtuoso® Spectre® Circuit Simulator and Accelerated Parallel Simulator User Guide Product Version 10.1.1 June 2011

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Virtuoso Spectre Circuit Simulator RF Analysis User Guide Product Version 6.2 June 2007

Virtuoso Spectre Circuit Simulator RF Analysis User Guide

Spectre Circuit Simulator User Guide January 2004 3 Product Version 5.0 Preface ...

Spectre Circuit Simulator User Guide - Columbia University The Virtuoso® Spectre® circuit simulator is a modern circuit simulator that uses direct methods to simulate

analog and digital circuits at the differential equation level. The basic capabilities of the Spectre circuit simulator are similar in function and application to SPICE, but the Spectre circuit simulator is not descended from SPICE.

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Reference

The Cadence ® Spectre ® X Simulator enables you to solve large-scale verification simulation challenges for complex analog, RF, and mixed-signal blocks and subsystems, while maintaining the accuracy expected of the Spectre simulation family. In addition, the Spectre X Simulator allows

you to massively distribute simulation workloads, enabling greater speed and capacity.

Spectre X Simulator - Cadence Design Systems

As the industry's leading solution for accurate analog simulation, the Cadence ® Spectre ® Simulation Platform

contains multiple solvers to allow a designer to move easily and seamlessly between circuit-, block-, and systemlevel simulation tasks. The foundation of the platform is a unified set of technologies shared by all of the engines—the parser, device models, Verilog-A behavioral ...

Spectre Simulation Platform - Cadence

Designed to help users create manufacturing-robust designs, the Cadence ® Virtuoso ® Analog Design Environment is the advanced design and simulation environment for the Virtuoso platform. It gives designers access to a new parasitic estimation and comparison

flow and optimization algorithms that help to center designs better for yield improvement and advanced matching and sensitivity analyses.

Virtuoso Analog Design Environment - Cadence Spectre Simulation Platform, eliminating model-correlation issues and simplifying

adoption of new simulation technologies. Using the same model equations means that silicon-accurate models from existing PDKs can be used with the Spectre X Simulator. Post-layout simulation The Spectre X Simulator uses proprietary techniques to

Spectre X Simulator - Cadence

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Design Systems

For more information, refer to the section on Monte Carlo Analysis in Chapter 6 of the Cadence Virtuoso Spectre Circuit Simulator User Guide, Product Version 5.1.41. The statistics Statement. The Spectre statistics control statement enables you to specify a batch-to-batch (process) and per-

instance (mismatch) variations for netlist parameters.

Process Variation and Mismatch - Keysight

The Cadence ® Spectre ® Accelerated Parallel Simulator provides scalable performance and capacity—at full Spectre Circuit Simulator accuracy—for

complex analog, RF, and mixed-signal blocks and subsystems with tens of thousands of devices.. The Spectre Accelerated Parallel Simulator performs advanced SPICE-accurate simulation with faster convergence, scalable performance, and higher capacity.

Spectre Accelerated Parallel

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Simulator

■Virtuoso Spectre Circuit Simulator User Guide Format Description PSF Format created by Virtuoso®Spectre Circuit Simulator (including the Virtuoso®Spectre RF Simulation Option) and other simulators integrated into the Virtuoso®Analog Design Environment. WSF Format created by SpectreVerilog.

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Environment User Guide" by Cadence. The mixed-signal design flow uses Cadence Virtuoso AMS environment and a set of tools tuned to facilitate the development of mixed-signal designs. The Virtuoso AMS environment and simulator work together to enable you to netlist, compile, elaborate, and simulate a circuit that contains analog, digital,

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Ghd Mixed-signal components.

Introduction to Mixed-Signal Simulation within Virtuoso ...
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